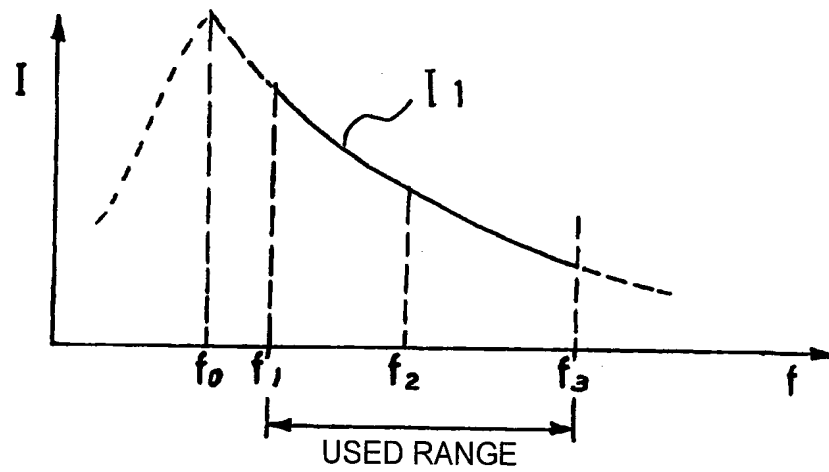


FIG. 1



**FIG. 2**

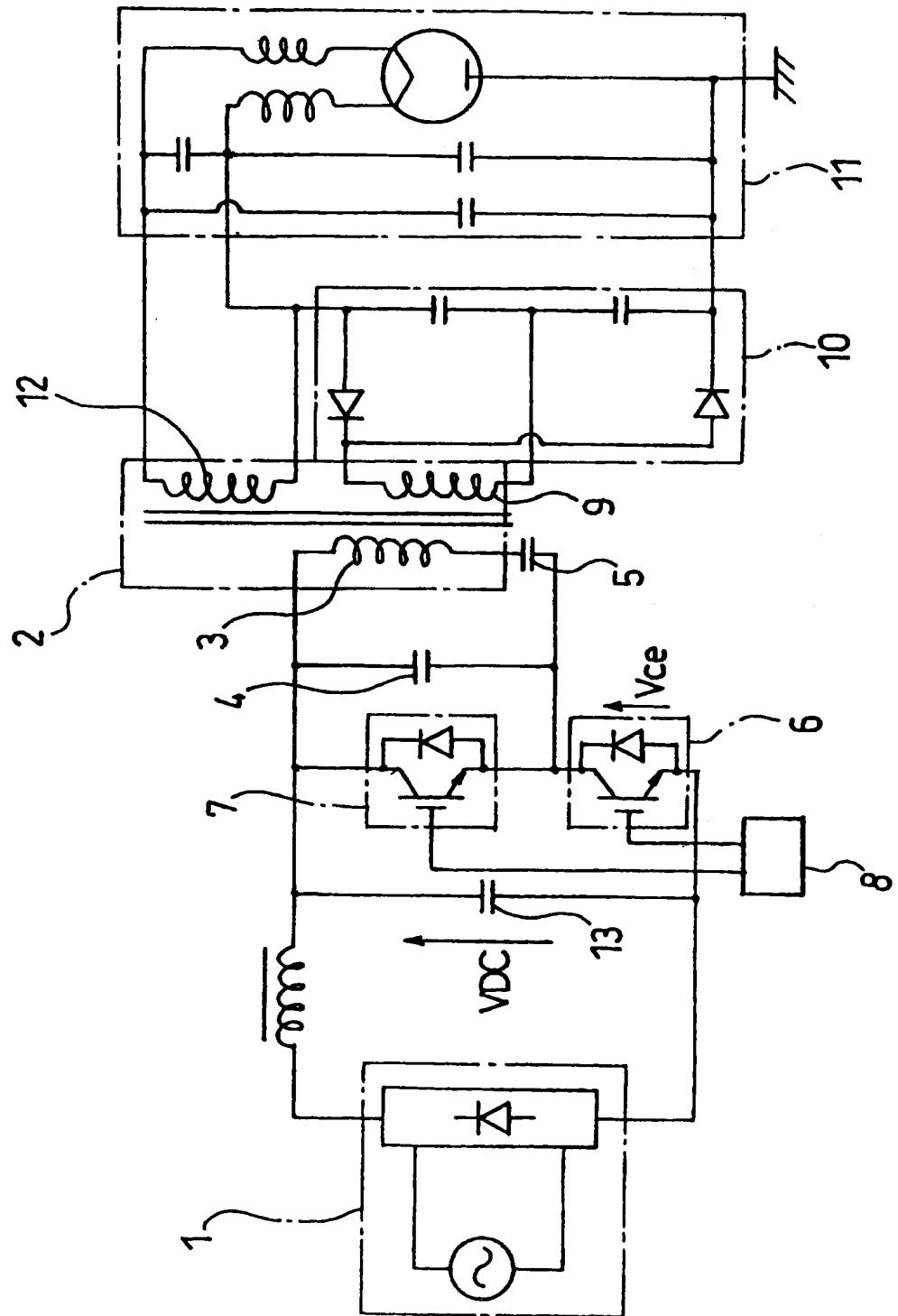


FIG. 3 (a)

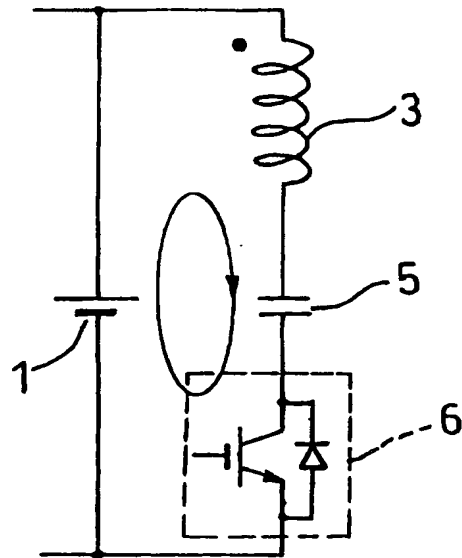


FIG. 3 (b)

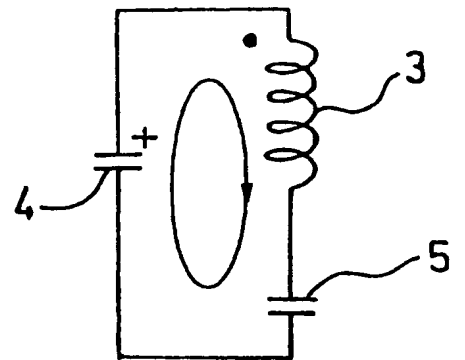


FIG. 3 (c)

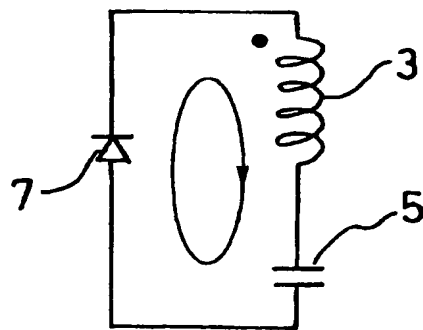


FIG. 3 (d)

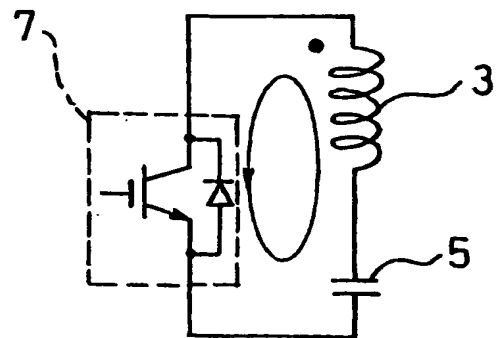


FIG. 3 (e)

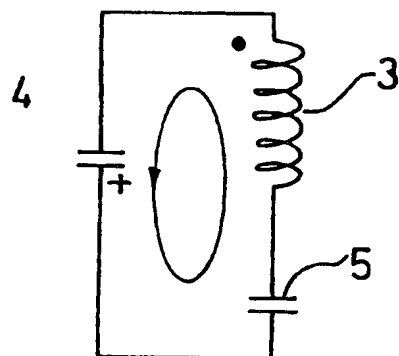


FIG. 3 (f)

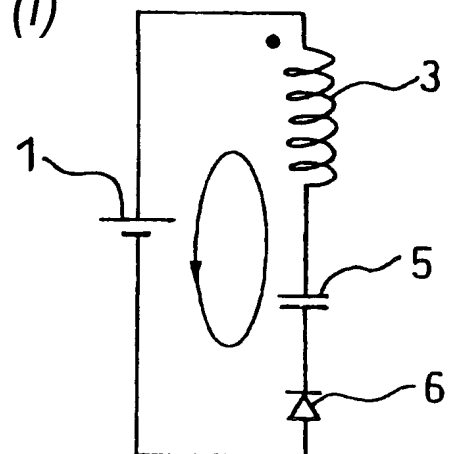


FIG. 4 (a) CURRENT OF FIRST SEMICONDUCTOR SWITCHING ELEMENT 6

FIG. 4 (b) VOLTAGE OF FIRST SEMICONDUCTOR SWITCHING ELEMENT 6

FIG. 4 (c) CURRENT OF SECOND SEMICONDUCTOR SWITCHING ELEMENT 7

FIG. 4 (d) VOLTAGE OF SECOND SEMICONDUCTOR SWITCHING ELEMENT 7

FIG. 4 (e) CURRENT OF FIRST CAPACITOR 4

FIG. 4 (f) VOLTAGE OF FIRST CAPACITOR 4

FIG. 4 (g) VOLTAGE OF SECOND CAPACITOR 5

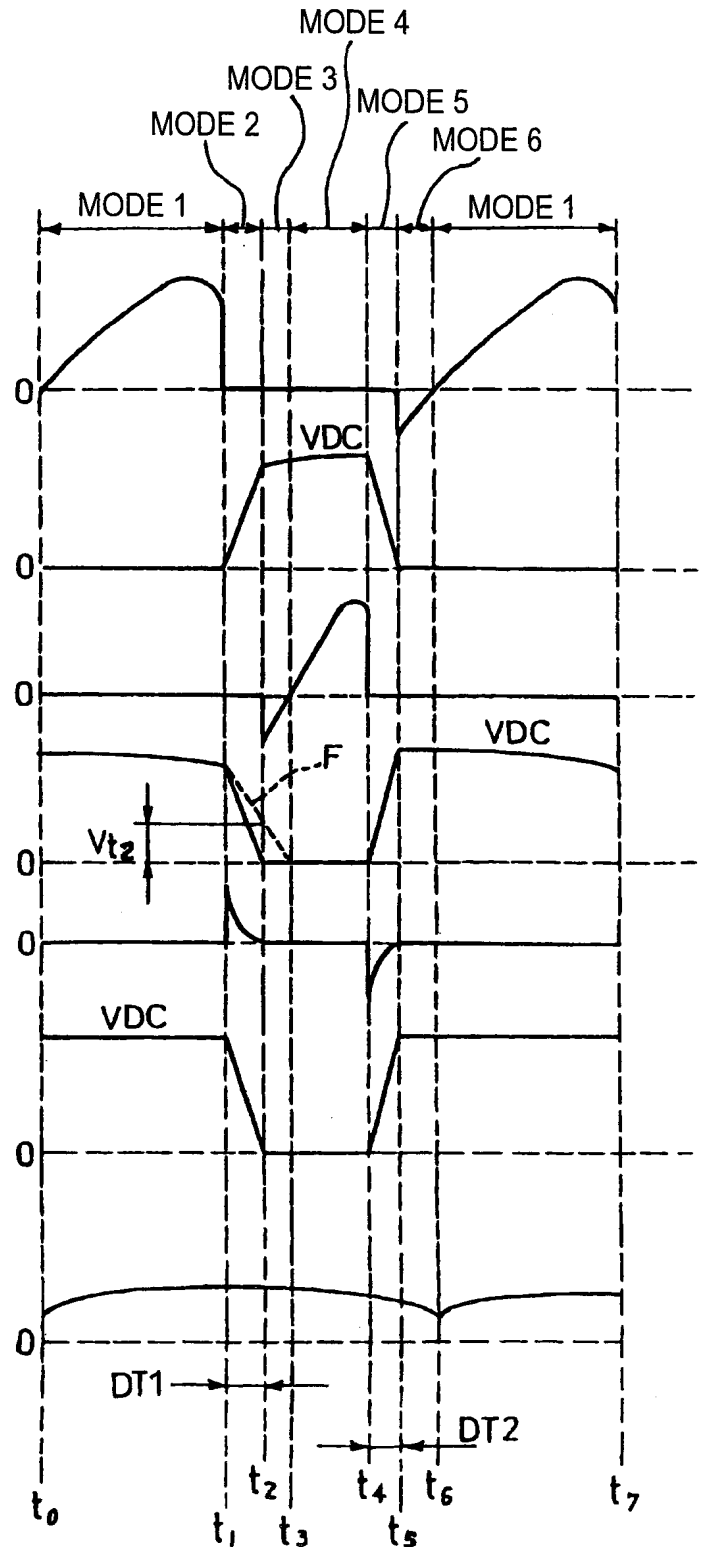


FIG. 5

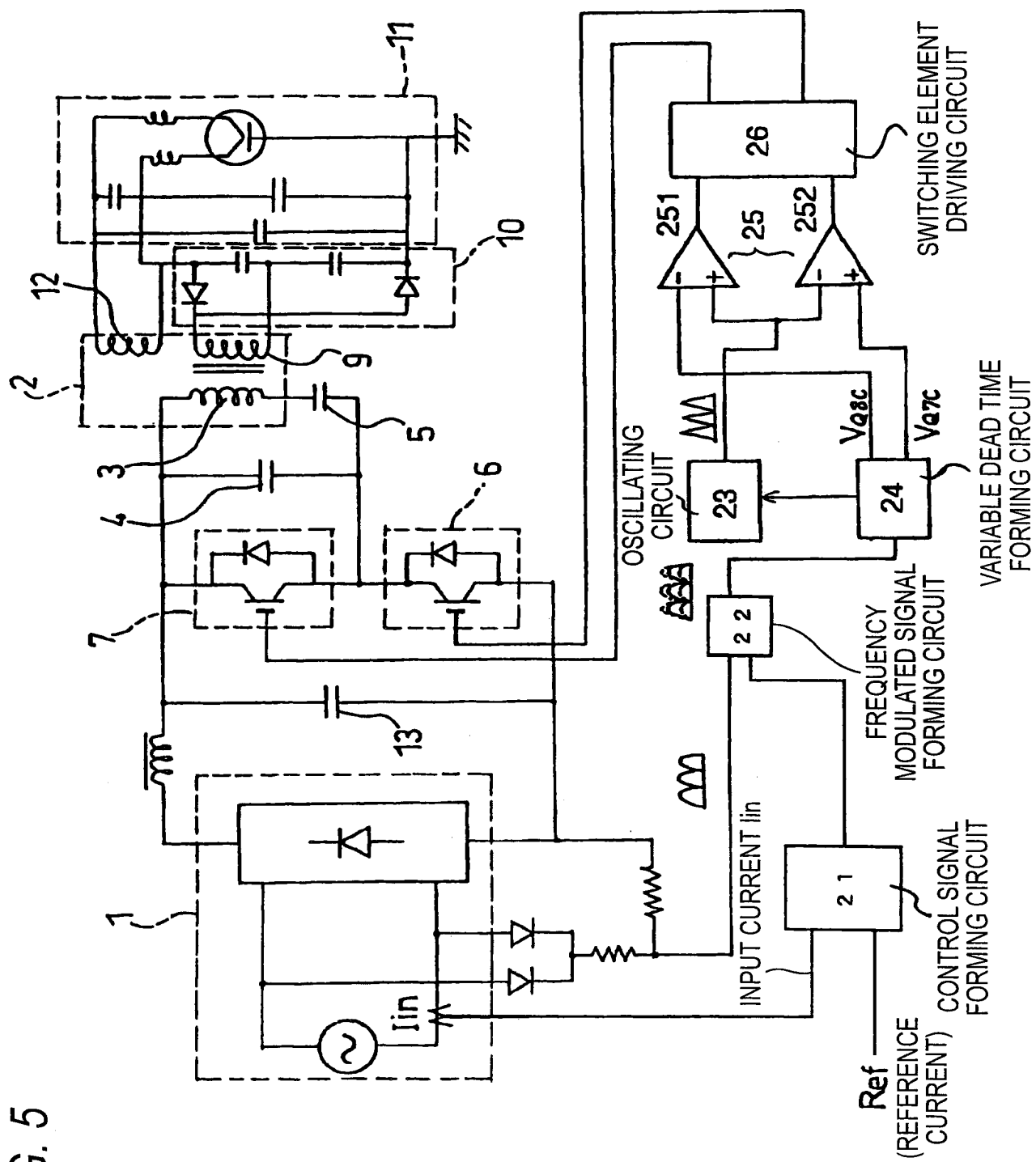


FIG. 6 (a)

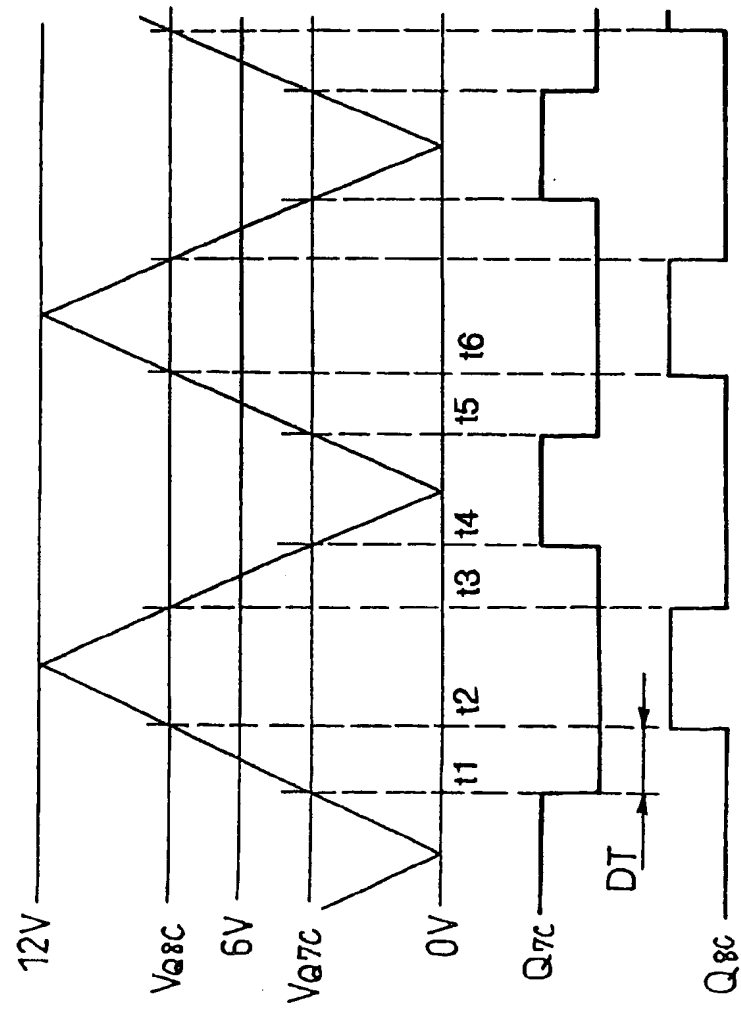


FIG. 6 (b)

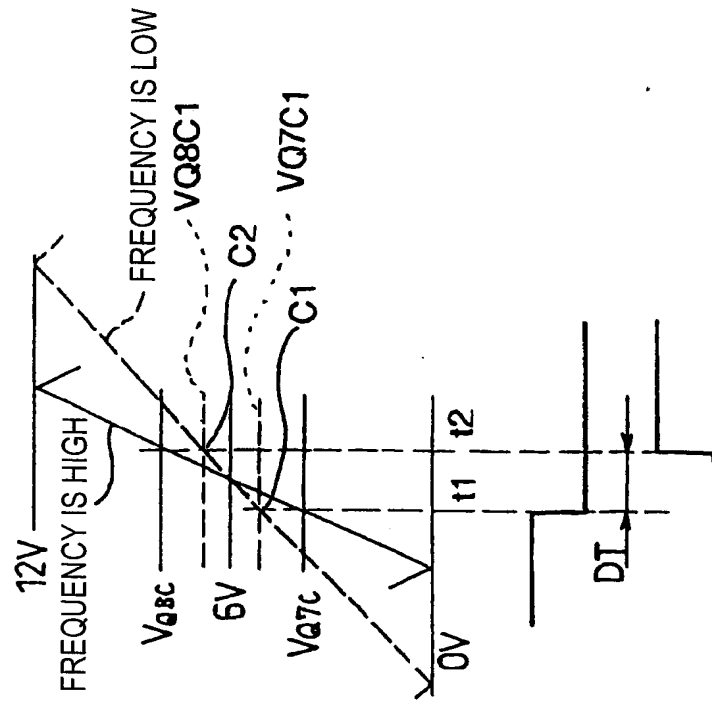


FIG. 7

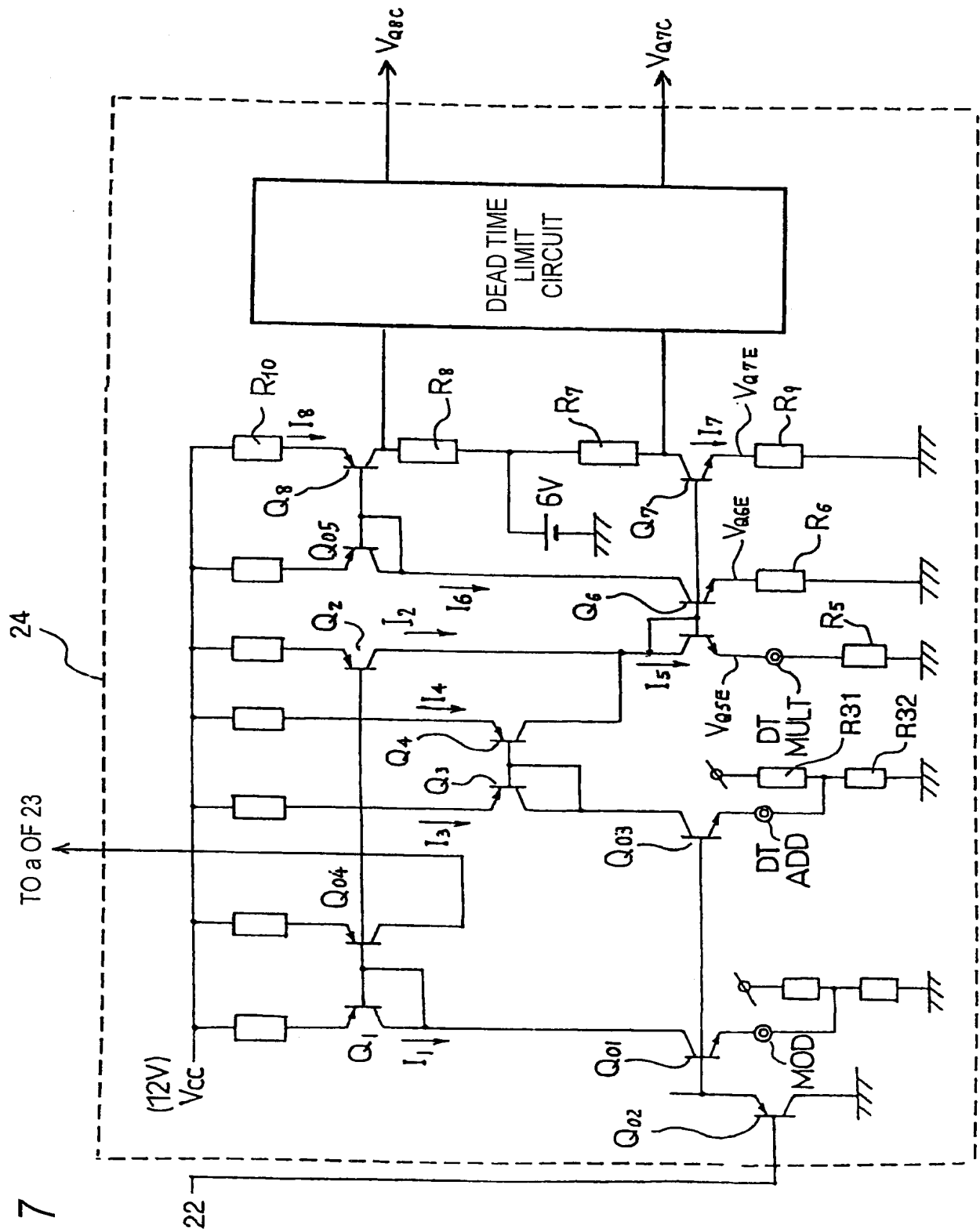
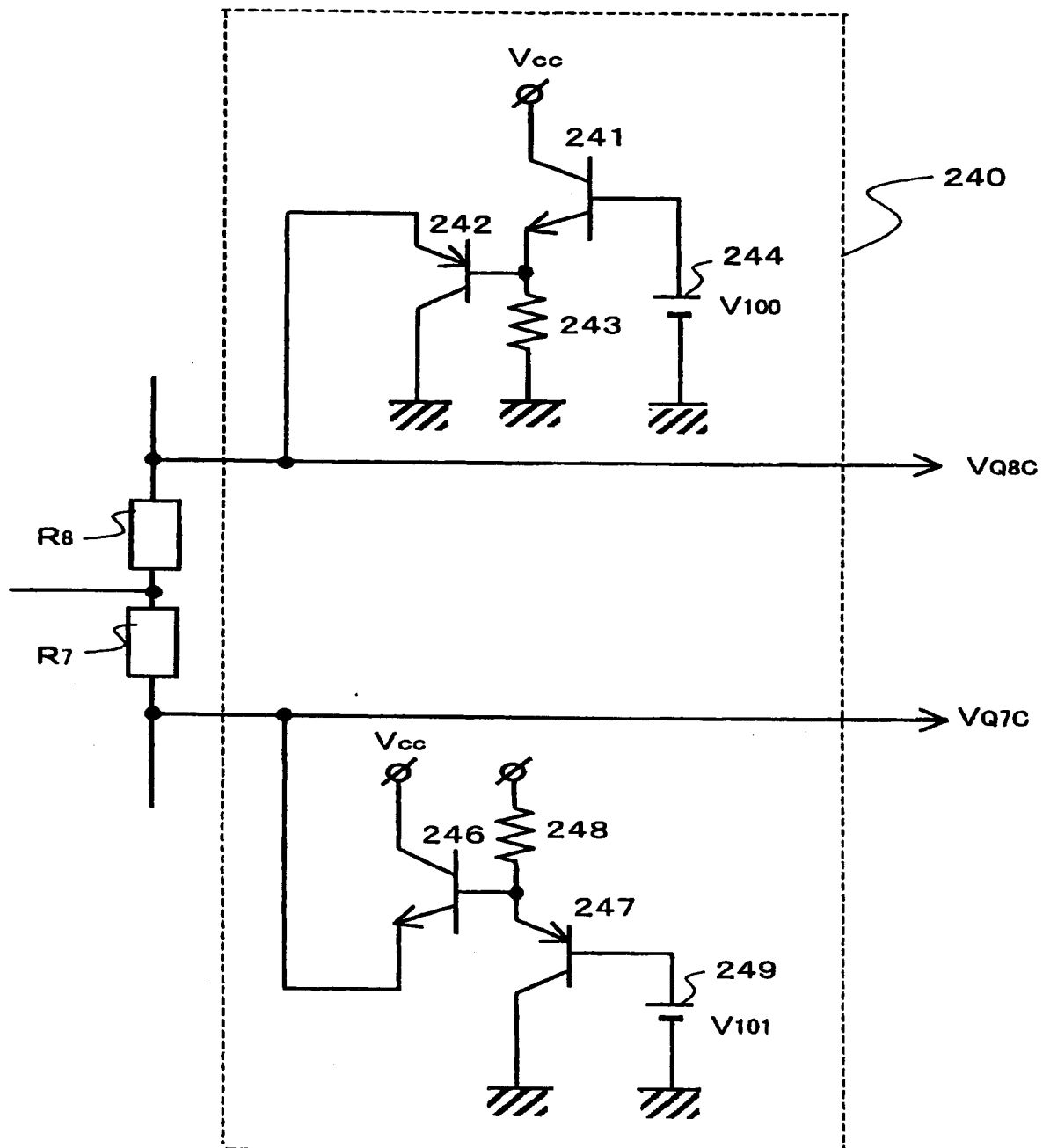


FIG. 8



(1)  $V_{100} \neq V_{101}$

(2)  $V_{100} < 12V$



FIG. 9

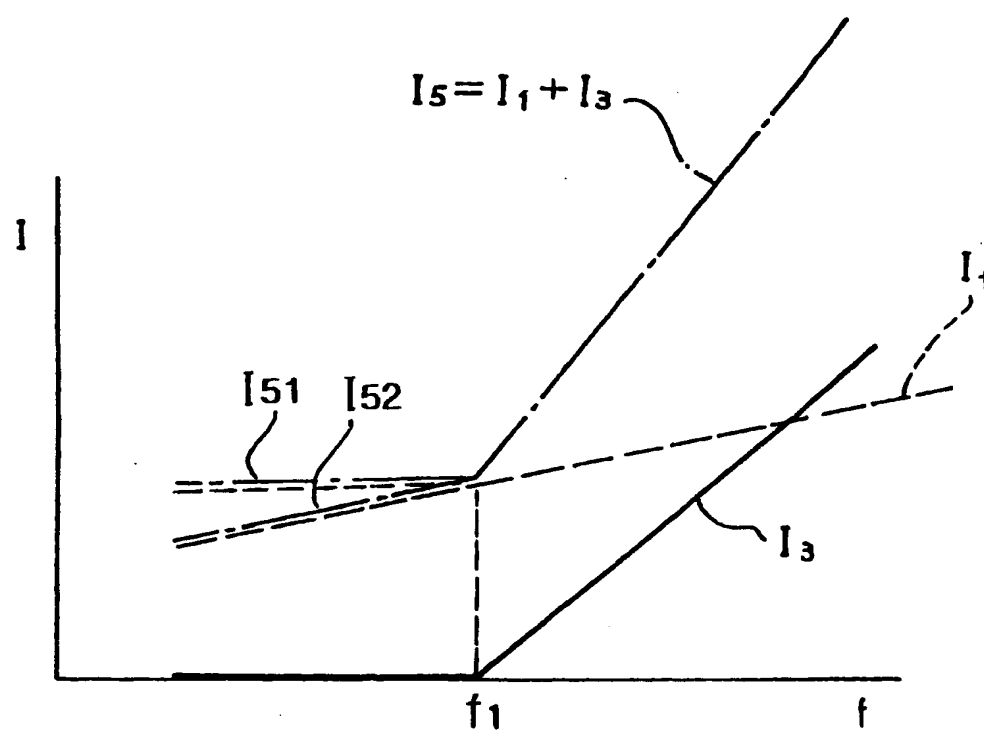


FIG. 10 (a)

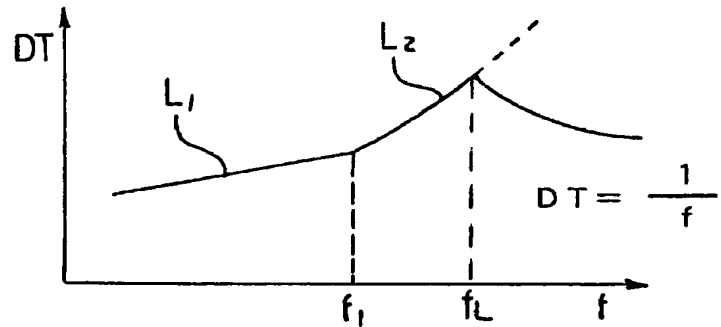


FIG. 10 (b1)

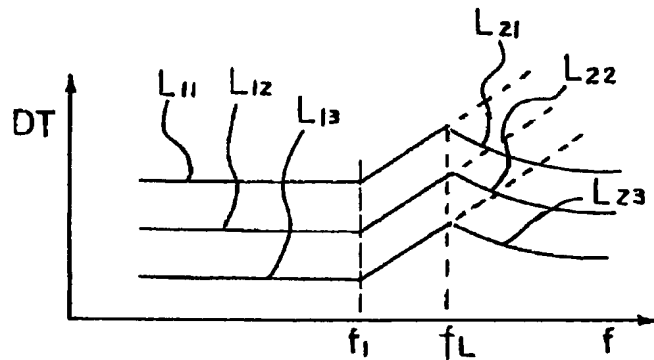


FIG. 10 (b2)

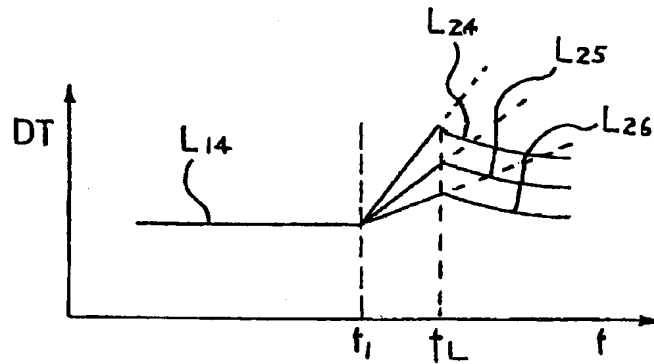


FIG. 10 (b3)

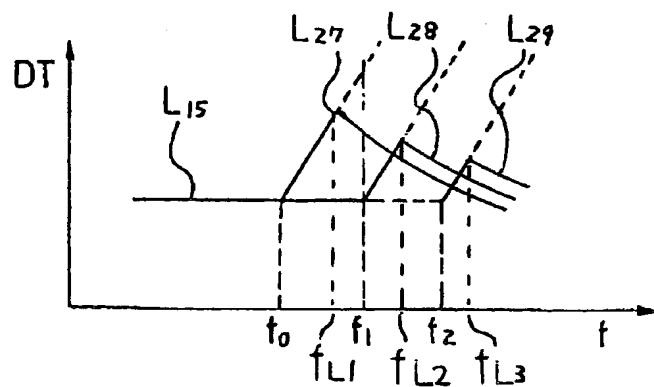
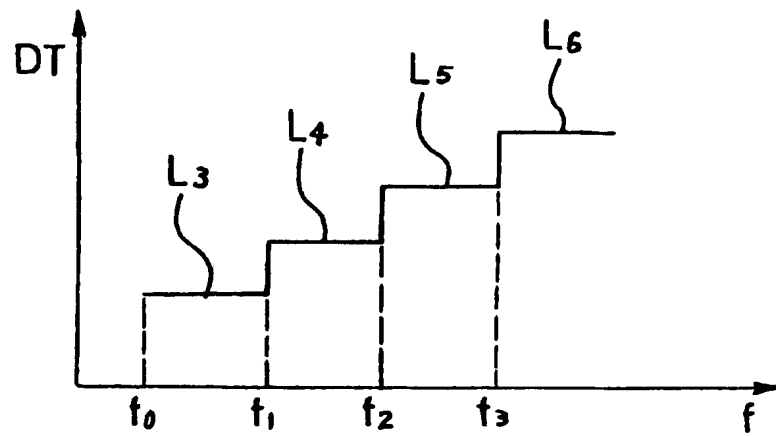


FIG. 11



**FIG. 12**

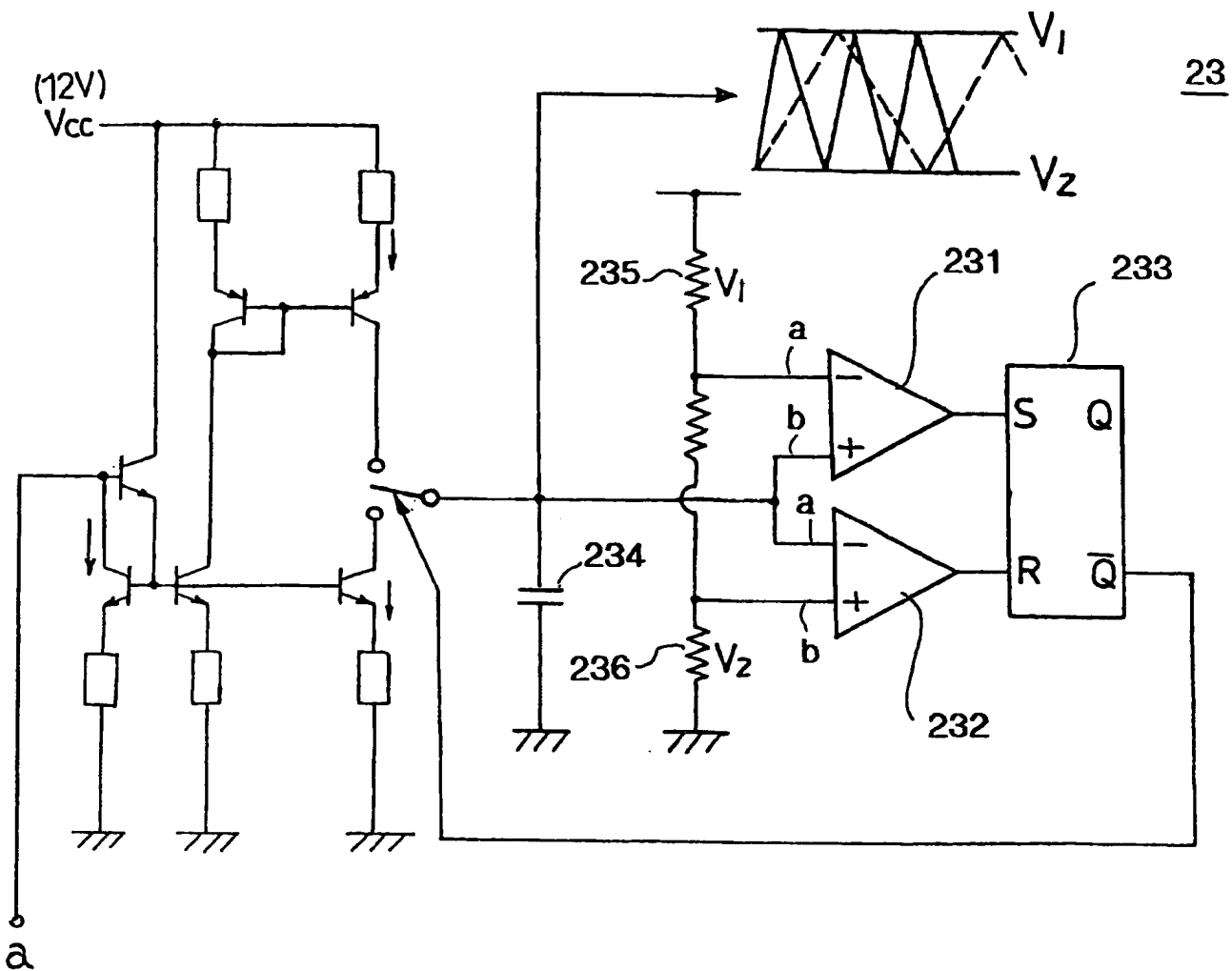


FIG. 13 (a)

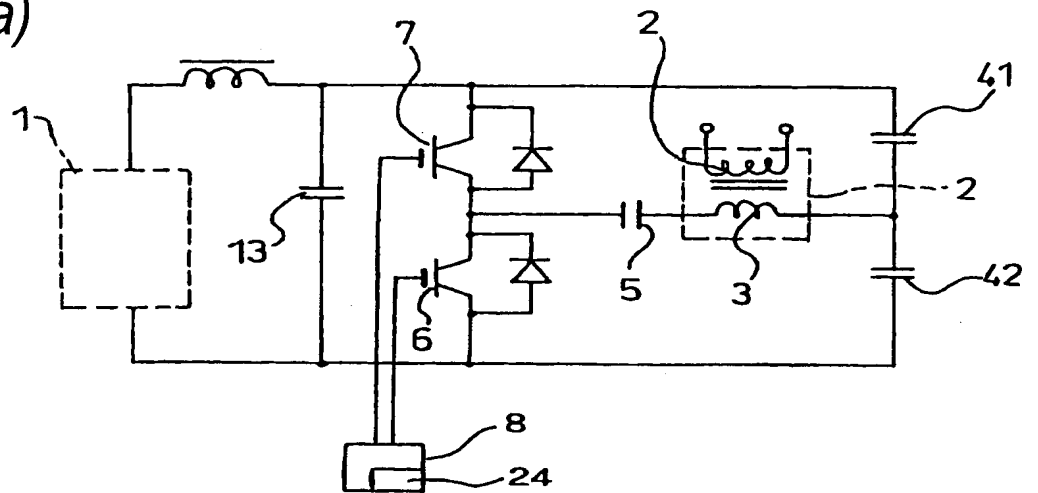


FIG. 13 (b)

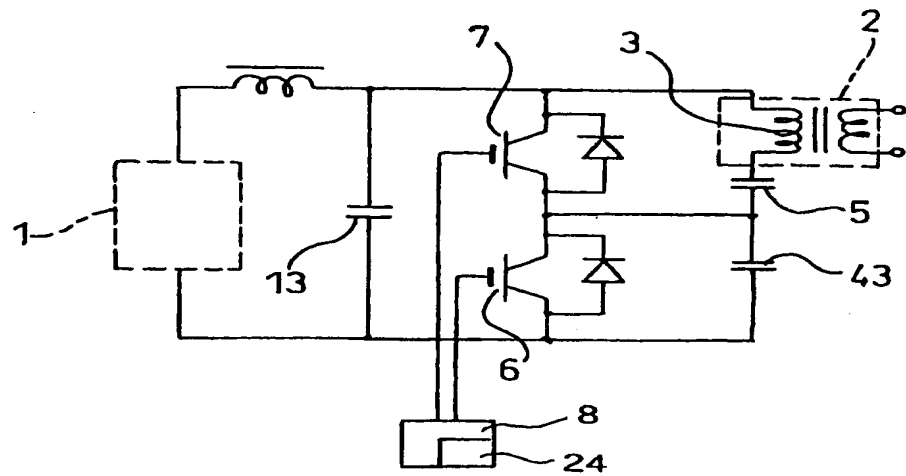


FIG. 13 (c)

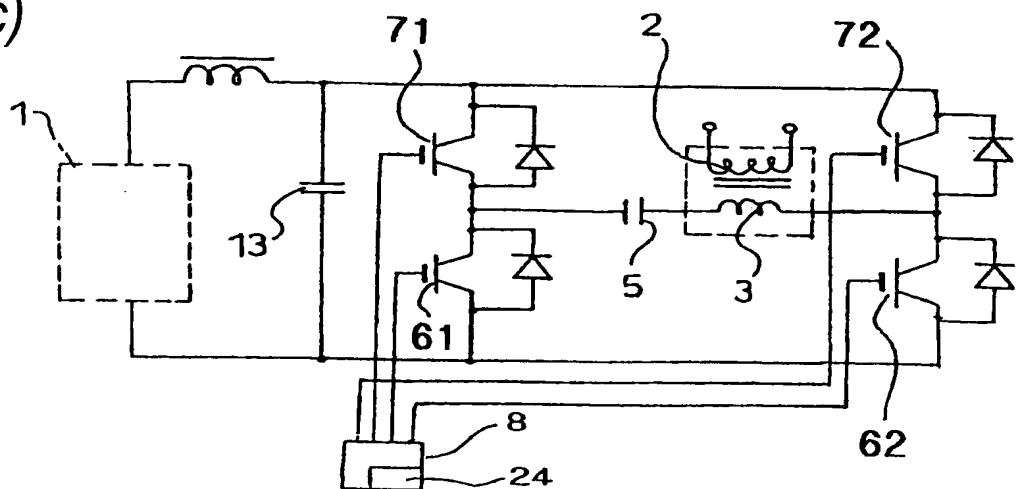


FIG. 14

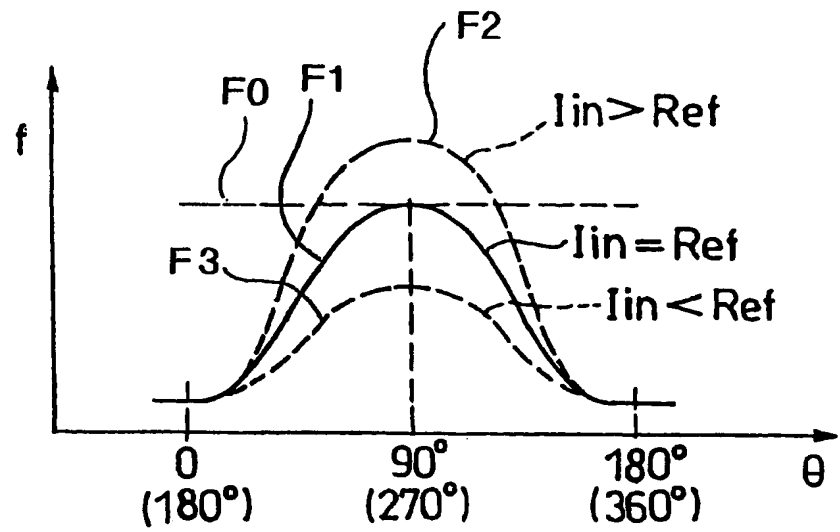


FIG. 15

